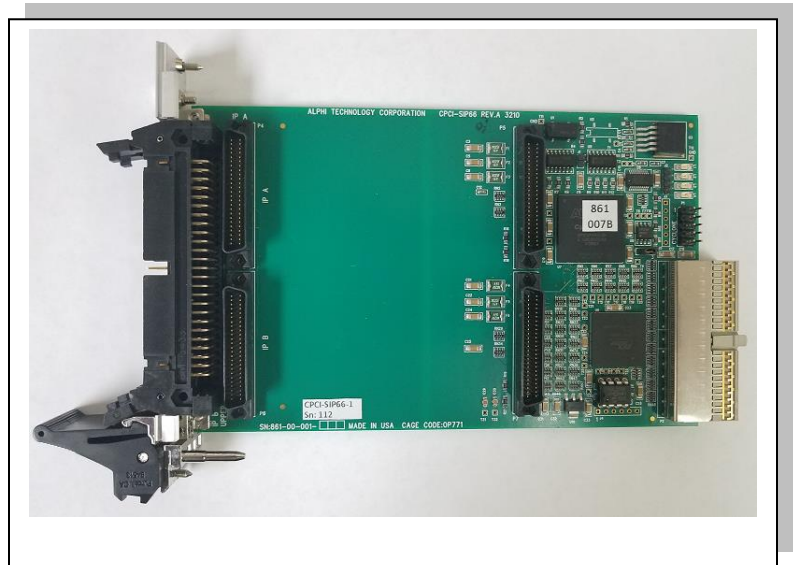


PXle Slave Dual Industry Pack Carrier

The PXle-SIP 3U module provides a high-performance flexible I/O scheme, that supports industry standard Industry Packs™. For applications requiring low cost, high density I/O or unique combinations, the PXle-SIP is the perfect solution

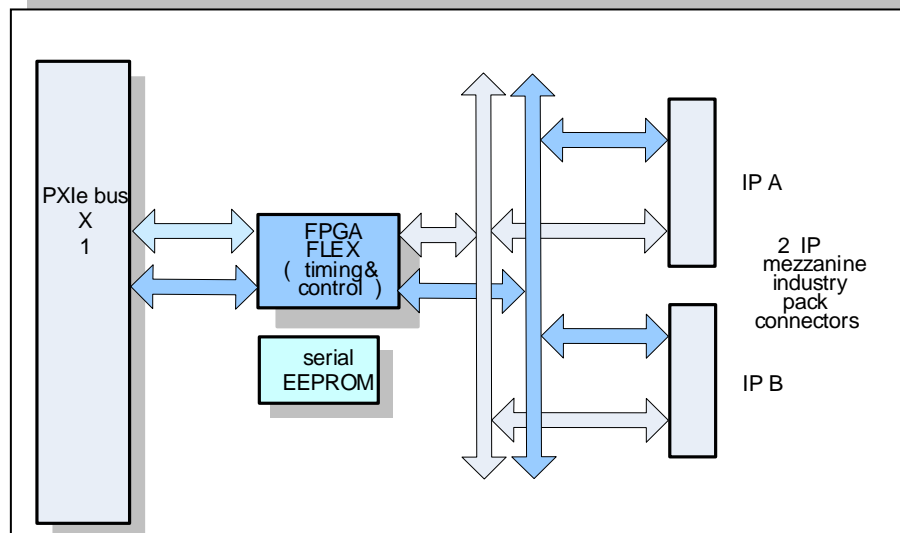
Features

- Slave dual industry pack carrier, PXle 3U format
- Support for up to two IP modules
- 8 MHz or 32 MHz IP operation Per slot software programmable
- Direct I/O or Memory mapped access from PXle bus via FPGA x1 PCIe
- Supports double-wide form factor Industry Pack
- Full interrupt support of host
- Front panel I/O connectors for all IP's; rear connector I/O also available
- Up to 96 I/O's on one single slot



Block Diagram Overview

The PXle-SIP is a 3U format slave Compact PXle bus IP carrier. The PXle-SIP provides mechanical support and the electrical interfaces for two single width IP modules, or one double width IP modules. Multiple PXle-SIP boards may be installed in a single system.



Available Software Drivers:

- Labview
- Linux
- Window XP

Applications:

For application requiring low cost, high density I/O or unique I/O combinations, the CPCI-SIP66 is the perfect solution.

Industry Pack Specifications:

- Meets ANSI/VITA 4-1995
- 8/32 MHz synchronous operation
- Supports ID, 128 byte I/O, interrupt. & 8 Mbyte memory spaces
- 2 Interrupts per module
- Two passive DMA channels are possible.
- Hardware self timed per IP module
- Triggered via system reset and software control
- Jumper or software time-out function
- 5, +/-12 volt reset-able fuse per IP
- 8/16 bit data on 3U board, 32 bit on 6U board

Operating Environment:

- Operating temperature
 Commercial: 0 to +70 °C
 Optional: -40 °C to +85 °C
- Non-operating: -55 °C to +100 °C
- Airflow requirement – 5 CFM
- Humidity – 5 to 90% (non-cond)
- Altitude – 0 to 10,000 feet

Mechanical Environment:

- Size – 3U CPCI module
 100mm x 160mm
- Power – 1 watt
- Vibration – 0.5G, 20-2000 Hz rand
- Shock – 20G, 11 msec, ½ sine
- Weight – 5 ounces
- MTBF – >250,000 hours

PCIe Bus Controller Features:

PCIe Bus:

- PICMG 1.1
- Zero wait state burst operation, with PCI bus bursts to 264 MB/sec and local bus bursts to 264 MB/sec
- 2 DMA channels
- Direct master data transfers
- Direct slave data transfers
- PCI bus INTA# interrupt. Up to 2 Interrupt from each IP

**For a complete list of Industry Packs offered by Alphi Technology please visit our WEB site at www.alphitech.com*



Ordering Information:

PXIe-SIP 3U PXIe Slave Industry Pack Carrier with Front I/O only
 PXIe-SIP -I Same as above but Industrial temp: -40 °C to +85 °C

Optional Accessories

CBL-68-VHDCI-L Cable 68 Pins VHDCI-68 , L = Length
 TB-68-VHDCI 68 Pins Terminal block