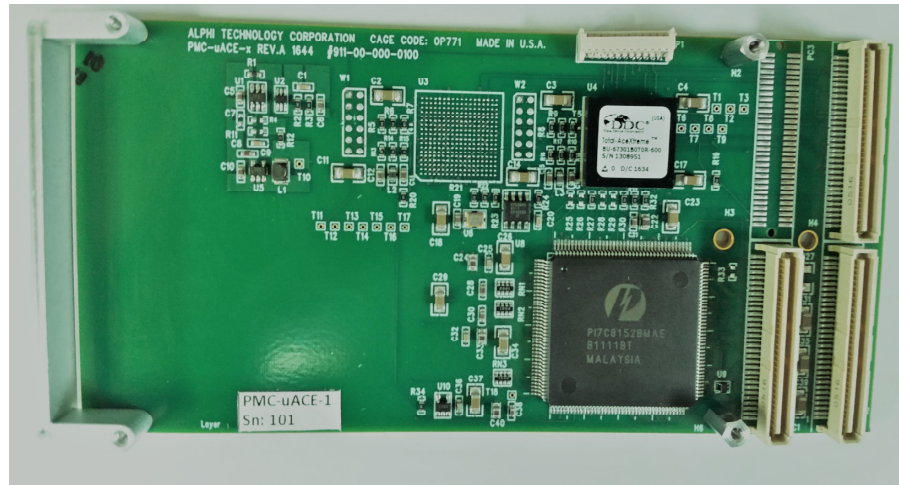


Summary

- MIL-STD-1553
- Options:
 - 0 to +70 degrees
 - 40 to +85 degrees
 - Conformal coating
- 128 Kbyte SRAM
- BC / RT / M modes
- Full Transparent Bridge
- (2) Dual Redundant Channel 1 (A+B)
Channel 2 (A+B)
- Selectable Internal / external CLK
- 78 ohm X-formers
- 5.0v or 3.3v VIO
- Low power
- Available Software
 - 'C' libs
 - Windows
 - Linux
 - VxWorks
- 33 Mhz PMC clock
- PICMG compliant



DUAL uACEX CONTROLLER BUS CONTROLLER, REMOTE TERMINAL, BUS MONITOR MODES



PMC-uACEx

Features

- 1 or 2 DDC uACE
- BU-67301B
- BC / RT / BM
- On-chip Transceivers
- On-chip Transformers

Memory

- 1 or 2 banks 128Kbytes Sram

Programmable via

- PMC Bus

PMC interface

- Transparent Bridge
- VIO 3.3 & 5.0 volt
- 33 Mhz
- 32 bit Addr. Data
- Interrupts
- DMA

I/O

- 1553 bus levels
- TTL (5v) clock
- Rear I/O

Operating: Environmental

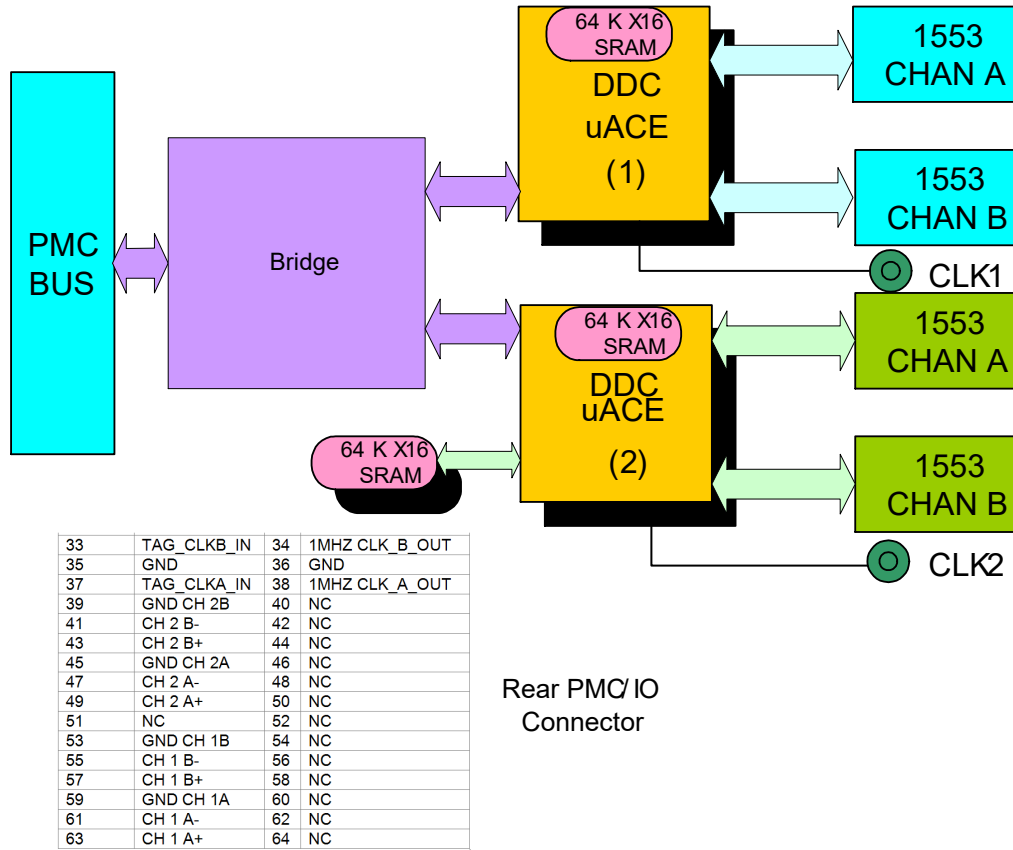
- Operating temperature
 - Commercial: 0 to +70 °C
 - Optional: -40°C to +85°C
- Airflow requirement: .5 CFM
- Humidity: 5 to 90% (non-cond.)
- Altitude: 0 to 20'000 ft

Non-Operating:

- Non-Operating temperature
-55°C to +105°

Mechanical: Environmental

- Size: Single wide PMC
2.92" x 5.87"
- Power: 1.5 watt
- Front panel I/O
- Vibration: 0.5G RMS
20-2000 Hz rand
- Shock: 20 G, 11 ms, 1/2 sine
- Weight: tbd
- MTBF: >250000 hours



Functional Block Diagram

Ordering Information

Part Number : PMC-uACE-1 (1) MIL-1553, BC/RT/M, DDC uAce
 Part Number : PMC-uACE-2 (2) MIL-1553, BC/RT/M, DDC uAce
 Part Number : PMC-uACE-x I Same as above with -40°C to +85 °C Temperature rating

Optional Accessories

Part Number : EngKit-1553-micro 2 T's, 2 Terminators, 1 16 inch cables
 Part Number : CBL-1553-micro Standard-Standard 16 inch 1553 cable
 Part Number : MIP-uACE-x PIM Module for Rear I/O Interface. Option -1 1 Ch, -2 2 Ch

Alphi Technology Corporation

1898 E. Southern Ave.
 Tempe, Arizona 85282, USA
 Tel: (480) 838 2428
 Fax: (480) 838 4477

823-01-000-4100

For ordering information: sales@alphitech.com Please visit our web site at www.alphitech.com