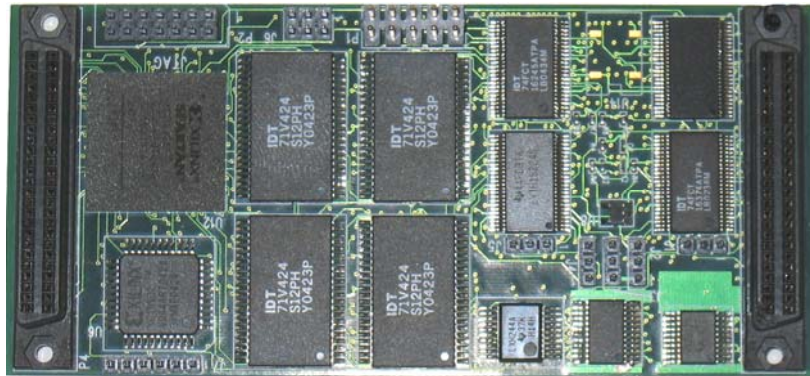


Summary

- XILINX FPGA IP Module
- Options:
SPARTAN IIE 50K to 400K gate devices
- 2 Mbyte SRAM
- SRAM is dual ported
- Separate logic for the IP interface
- 48 selectable I/O
- Reconfigurable
- LVDS, LVTTTL, LPECL LVCOMS drivers
- User configurable IO by groups
- Programmable via IP, HW-USB, HW-PCi
- Local serial Eprom
- Internal / External clock
- 8/32 Mhz IP clock support
- Vita 4 compliant



XILINX SPARTAN FPGA, 2MByte DUAL-PORTED SRAM LVDS, LVTTTL, LPECL, LVCMOS, I/O DRIVERS



IP-XILINX-SPARTAN

Features

- XILINX Spartan IIE XC2S50E - XC2S400E supported
- Stand alone capability
- 2 Mbyte SRAM
- 48 selectable I/O

Memory

- 2 Mbyte external SRAM
- Dual ported between the XILINX and IP bus with arbitration

I/O Level

- LVDS
- LVTTTL
- LPECL
- LVCMOS

Programmable via

- HW-USB, HW-PCi
- Serial Eprom
- IP Bus

IP interface

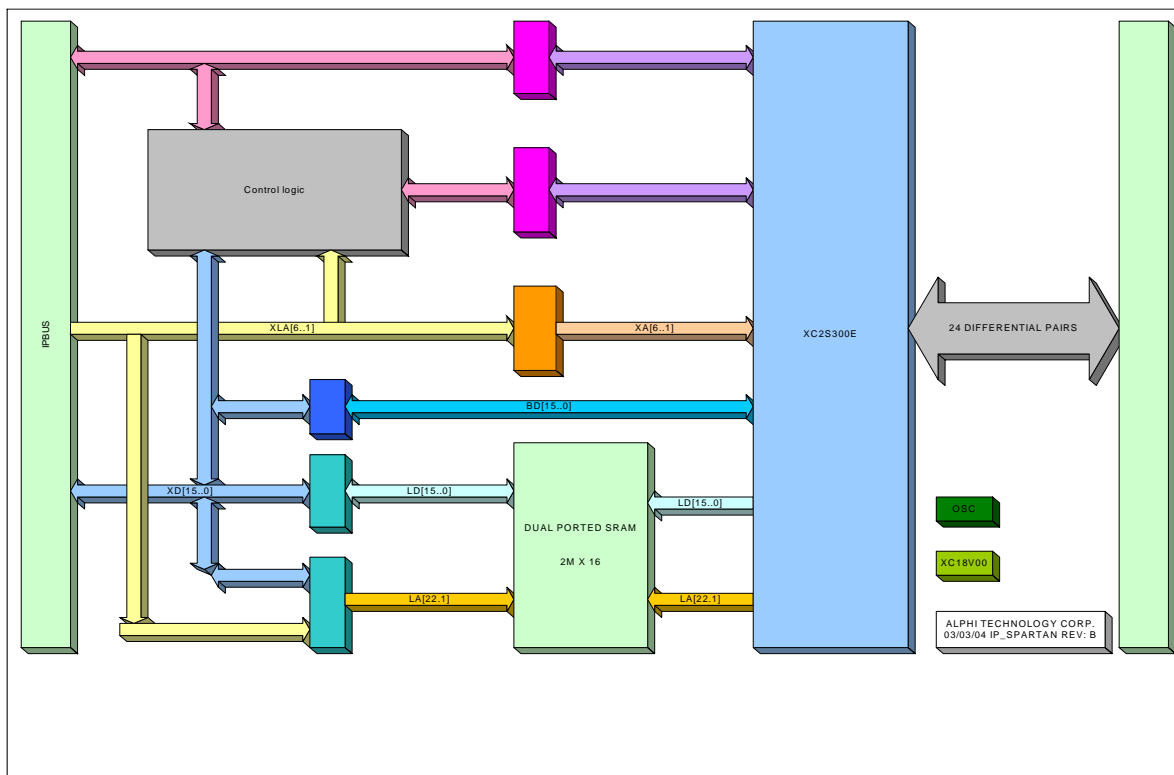
- 8/32 Mhz clock
- 16 bit data
- Interrupts
- DMA

Operating: Environmental

- Operating temperature
Commercial: 0 to +55 °C
Optional: -25°C to +80°C
- Non-operating: -40°C to 85 °C
- Airflow requirement: CFM
- Humidity: 5 to 90% (non-cond.)
- Altitude: 0 to 10'000 ft

Mechanical: Environmental

- Size: Vita 4 Type 1 IP
1.8" x 3.9"
- Power: watt
- Vibration: 0.5G RMS
20-2000 Hz rand
- Shock: 20 G, 11 ms, ½ sine
- Weight: tbd
- MTBF: >250000 hours



Functional Block Diagram

Ordering Information

Part Number :	IP-XILINX-SPARTAN-1	50K gate SPARTAN Industry Pack module
Part Number :	IP-XILINX-SPARTAN-2	100K gate SPARTAN Industry Pack module
Part Number :	IP-XILINX-SPARTAN-3	200K gate SPARTAN Industry Pack module
Part Number :	IP-XILINX-SPARTAN-4	400K gate SPARTAN Industry Pack module

Optional Accessories

Part Number :	TB-50-HDR	50 pin terminal block and 1meter flat ribbon cable
Part Number :	CBL-50-HDR	50 pin, 1meter flat ribbon cable, IDC header connector

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