

PMC MIL-STD-1553 BC/RT/MT



FEATURE SUMMARY

- Single / Dual channel Dual Redundant
- Fully integrated MIL-STD-1553 interface
- DDC ACE controller
- STANAG 3838 Compliant
- PLX 9080 bus interface
- 64K word SRAM
- On board transceivers & transformers
- External clock inputs per channel
- Flexible processor / memory interface
- Software libraries and drivers are available
- Front panel I/O connectors
- Single wide PMC form factor

OVERVIEW

The PMC-1553-DDC-1/2 implements a complete single channel dual-redundant MIL-STD-1553 bus terminal on a single wide PMC module. Full BC/MT/RT modes are supported. 0°C to 70°C operating temperature is standard. Included is an DDC ACE intelligent communications protocol engine, dual transceivers and encoder / decoders, shared memory, and management logic. The PMC-1553-DDC is designed with an advanced communications engine, DDC's ACE series hybrid, plus two interface transformers. Support for both direct coupled (short stub) and transformer coupled (long stub).

ORDERING INFORMATION

p/n:	PMC-1553-DDC-1	Single, dual redundant
	PMC-1553-DDC-2	Dual, dual redundant
	EngKit-1553	(2ea.)T's, Terminators, cables
	CBL-1553-1	2M, std-std 1553 cable
	CBL-1553-2	2M, std-mini 1553 cable
	CBL-1553-3	2M, mini-mini 1553 cable

