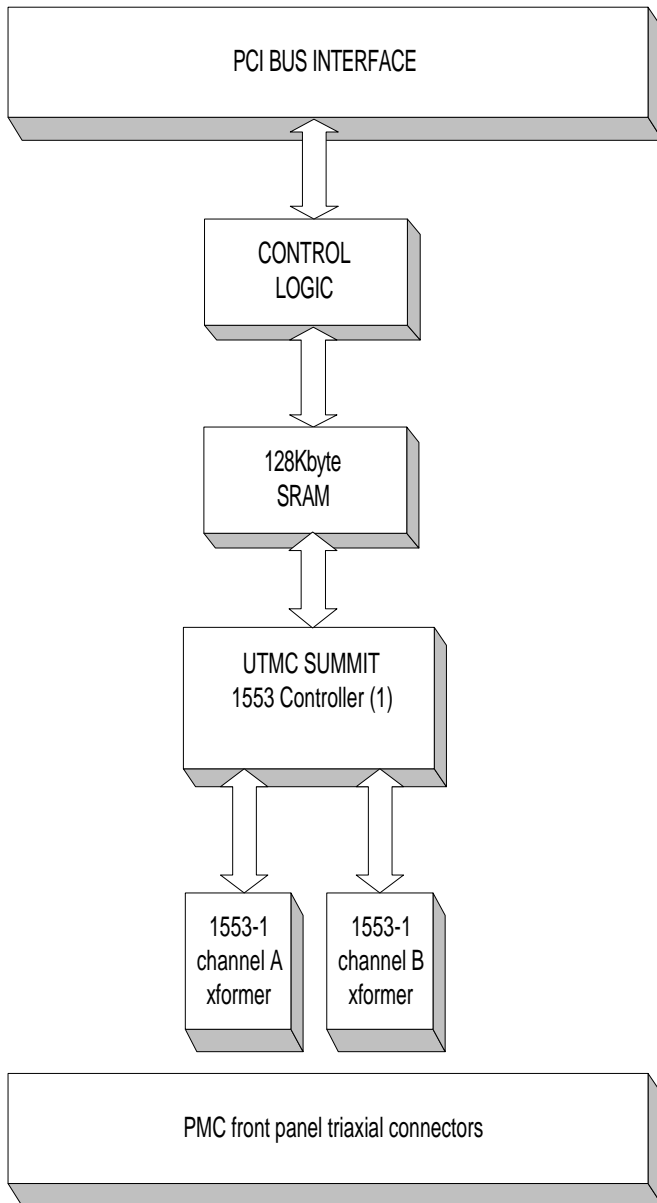


MIL-1553 PCI with SUMMIT Controller Bus Controller / Remote Terminal / Bus Monitor

PCI-1553B
Functional Block



FEATURE SUMMARY

- MIL-STD-1553B Notice II Remote Terminal
- UTMC SUMMIT chip
- Simultaneous Remote Terminal/Monitor mode
- Full 16-bit (R/W) time-tag with user-defined resolution
- Dual redundant 1553B BC/RT/BM modes
- Dual triaxial front panel connectors
- Multiple message processing in BC and RT modes
- Time tagging and message logging in RT mode
- Commercial, Industrial temperature ranges

OVERVIEW

The PCI-1553 board gives the MIL-STD-1553B designer a versatile solution to multiplexed 1553 data bus design requirements. In applications that require the inherent performance and reliability of 1553B communications, the PCI-1553 is a cost-effective system component with unsurpassed performance. The PCI-1553 board minimizes host overhead with its automatic execution of message transfers, interrupt handling, and generation of status information. In the Bus Controller mode, the PCI-1553 implements a linked list message scheme to provide the system with message chaining capability. In the Remote Terminal mode, time tagging and message history functions are available. The PCI-1553 also allows multiple message processing, programmable automatic message delay, and automatic polling and retry.

ORDERING INFORMATION

P/N:	PCI-1553B	BC/TR/BM, single PCI
	EngKit-1553	(2ea.)T's, Terminators, cables
	CBL-1553-1	2M, std-std 1553 cable
	CBL-1553-2	2M, std-mini 1553 cable
	CBL-1553-3	2M, mini-mini 1553 cable

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