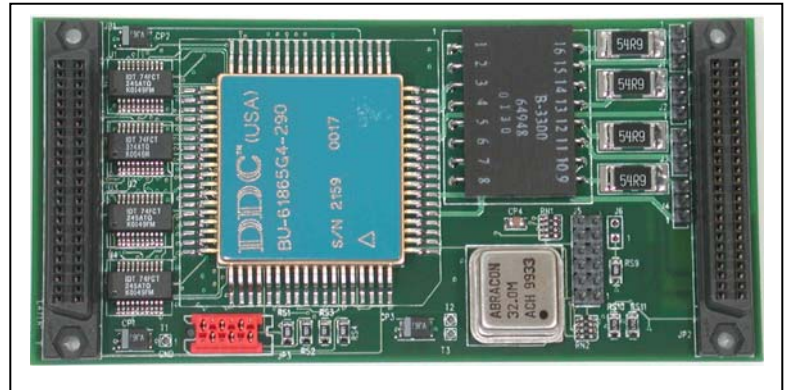


IP DDC 1553 Controller

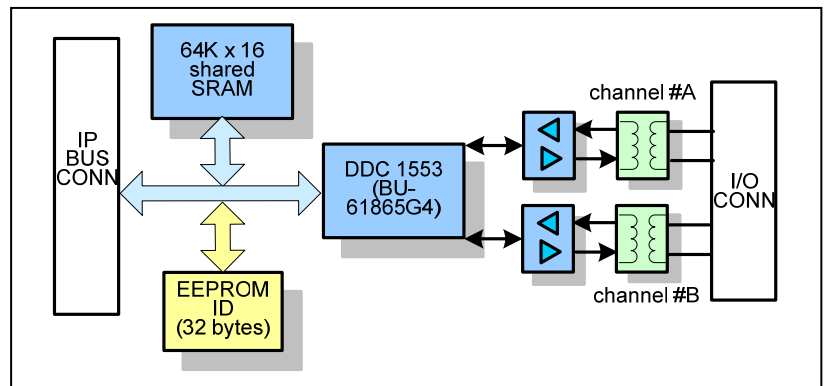
Features

- 1 controller of dual redundant (A/B channel) DDC 1553 communications
- Programmable bus controller, remote terminal, or monitor terminal modes
- Long or short stub support
- Low power consumption
- On-chip transceivers
- On-chip 64 Kbyte of SRAM
- On-board isolation transformers
- Single +5 VDC supply
- EEPROM (32 bytes) for identification
- RT address and operational modes program or jumper selectable
- 8 or 32 MHz clock
- 2 interrupts and 2 slave DMA IP bus lines
- VITA 4 compliant
- Supports MIL-STD-1553A, MIL-STD-1553B Notice 2, STANAG 3838, and McAir A3818, A5232 and A5690 protocols.



Block Diagram Overview

The IP-1553-DDC uses the DDC BU-61865G4-290 1553 communication device as its 1553 bus controller, or remote terminal, or monitor terminal. A single controller has two redundant channels and built in transceivers. The controller accesses 64Kx16 word of internal dual port memory and has internal transceivers for both channel A and B. The IP board has on-board transformers.



Available Software Drivers:

- C library dll's
- Linux drivers
- Window XP drivers
- VxWorks drivers

Applications:

This is a perfect solution for a wide array of 1553 communication applications such as:

- Test equipment supporting evaluation, simulation, monitoring, and analysis,
- Operational equipment such as avionics, space satellite systems, aircraft onboard systems, commercial systems, etc.
- Applications that require dual functionality of the terminal for monitoring and remote operation at the same time

1553 Compliance/Characteristics:

- Data Rate – 1 MHz
- Word Length – 20 bits
- Data Bits/Word – 16 bits
- Message Length – maximum of 32 data words
- Transmission Technique – half-duplex
- Operation – asynchronous
- Transmission encoding – Manchester II, bi-phase
- Protocol – command/response
- Bus Control – single or multiple
- Fault tolerance – typically dual redundant, second bus in “hot backup” status
- Message formats – controller to terminal, terminal to controller, terminal to terminal, broadcast, system control
- Number of remote terminals – maximum 31

- Terminal Types – remote terminal, bus controller, bus monitor
- Transmission Media – twisted shielded pair
- Coupling – transformer and direct

1553 Communication Device:

- DDC BU-61865G4-290 BC/RT/MT configured
- 64K-word internal SRAM, with memory parity.
- 5V internal transceiver and logic.
- BC architecture providing functionality for highly autonomous message sequence control, and general purpose queue
- RT global circular buffer, interrupt status queue and auto-boot option
- Reduced maximum hold-off time for shared RAM host interface.
- Increased request-to-grant time for DMA
- Autonomous built-in self-test.

Industry Pack Specifications:

- Meets ANSI/VITA 4-1995
- 8/32 MHz synchronous operation
- Supports ID, 128 byte I/O, interrupt. & 8 Mbyte memory spaces
- 2 Interrupts per module
- Two passive DMA channels are possible.
- Hardware self timed per IP module
- Triggered via system reset and software control
- Jumper or software time-out function
- 5, +/-12 volt reset-able fuse per IP

Mechanical: Environmental:

- Size – VITA 4 compliant
1.8" x 3.9" or 46 mm x 99 mm
- Power – 1.0 watt
- Vibration – 0.5G, 20-2000 Hz rand
- Shock – 20G, 11 msec, 1/2 sine
- Weight – 2 ounces
- MTBF – >250,000 hours

Operating Environment:

- Operating temperature
Commercial: 0 to +70 °C
Optional: -25 °C to +80 °C
- Non-operating: -40 °C to +85 °C
- Airflow requirement – 5 CFM
- Humidity – 5 to 90% (non-cond)
- Altitude – 0 to 10,000 feet

Terminal Electrical Input Characteristics:

Requirements	Transformer coupled	Direct Coupled
Input level ¹	0.866-14.0V	1.2-20.0V
No response ¹	0.0-0.2V	0.0-0.28V
Zero crossing stability	+/-150.0 nSec	+/_ 150.0 nSec
Rise/fall times	0 nsec – Sine	0 nsec – Sine
Noise rejection ²	140 mV WGN	200 mV WGN
Common mode rejection ³	+/- 10.0V peak	+/- 10.0V peak
Input impedance ⁴	1000 ohms	2000 ohms

Notes on measurement conditions:

1. p-p, I-I
2. BER 1 per 10⁷
3. Line-ground DC-2 MHz
4. 75 KHz-1MHz

Terminal Electrical Output Characteristics:

Requirements	Transformer coupled	Direct Coupled
Output level ¹	18.0-27.0V	6.0-9.0V
Zero crossing stability	25.0 nsec	25.0 nsec
Rise/fall ²	100-300 nsec	100-300 nsec
Max distortion ³	+/-900.0 mV	+/- 300.0 mV
Max output noise ⁴	14.0 mV	5.0 mV
Max residual voltage ³	+/-250.0 mV	+/-90.0 mV

Notes on measurement conditions:

1. p-p, I-I
2. 10%-90%
3. peak, I-I
4. rms, I-I



Ordering Information:

Part number: IP-1553-DDC 1553 Industry Pack
 EngKit-1553 (2ea.)T's, Terminators, cables

Optional Accessories:

Part number: CBL-1553-1 2M, std-std 1553 cable
 CBL-1553-2 2M, std-mini 1553 cable
 CBL-1553-3 2M, mini-mini 1553 cable